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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/680,420	10/08/2003	Kyung-Shik Lee	1793.1037	4326

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EXAMINER	
NGUYEN, KEVIN M	
ART UNIT	PAPER NUMBER
2629	

DATE MAILED: 05/17/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/680,420	Applicant(s) LEE, KYUNG-SHIK	
	Examiner Kevin M. Nguyen	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *08 October 2003*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-41* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-41* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *08 October 2003* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *11/19/2004*.

4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .

5) ☐ Notice of Informal Patent Application (PTO-152)

6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-41 are rejected under 35 U.S.C. 102(e) as being anticipated by Shirasaki et al (US 6,943,753) hereinafter Shirasaki.

3. As to claim 1, Shirasaki conventionally discloses an apparatus to output different display identification data depending on a connector type [DVI-I connector, Fig. 13], comprising:

an analog display identification data storing and outputting unit [1K bit EEPROM-EDID data of analog specification, see Fig. 13] which stores predetermined analog display identification data and outputs the stored analog display identification data to a predetermined system when a command to output the analog display identification data is issued;

a digital display identification data storing and outputting unit [1K bit EEPROM-EDID data of digital specification, Fig. 13] which stores predetermined digital display identification data and outputs the stored digital display identification data to the predetermined system when a command to output the analog display identification data is issued;

a connector type identification unit [DVI-I interface, see Fig. 12, col. 2, lines 15-22] which identifies a connector type based upon an output of pins of a predetermined connector; and

a display identification data output command unit [the user makes setting on the switcher, see Fig. 13] which commands one of: the analog display identification data storing and outputting unit, and the digital display identification data storing and outputting unit, to output one of:

the analog display identification data [the analog EDID], and

the digital display identification data [the digital EDID], to the

predetermined system based upon an identified connector type [DVI type, column 1, line 10 through columns. 2-3, line 19].

4. As to claim 2, Shirasaki conventionally discloses the apparatus of claim 1, wherein the analog display identification data and the digital display identification data have a predetermined structure according to extended display identification data (EDID) specifications of the Video Electronics Standards Association (VESA)[1] [see column 1, lines 17-23].

5. As to claim 3, Shirasaki conventionally discloses the apparatus of claim 1, wherein the predetermined connector is a DVI connector [DVI connector, see Fig. 12, column 1, line 54 to column 2, line 8].

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[1] See http://en.wikipedia.org/wiki/EDID

6. As to claim 4, Shirasaki conventionally discloses the apparatus of claim 3, wherein the connector type identification unit identifies the DVI connector as one of a DVI-A-type, a DVI-D-type, and a DVI-I-type connector based on the output of pins of the DVI connector [see Fig. 12, column 1, line 54 to column 2, line 8].

7. As to claim 5, Shirasaki conventionally discloses the apparatus of claim 4, wherein the connector type identification unit comprises: a DVI-A-type identifier which identifies the DVI connector as a DVI-A-type connector when an output of a 22nd of the DVI connector of a Terminal Display Management System clock ground is at a high impedance and an output of a C5-th pin of the DVI connector of an analog ground is at a ground level; a DVI-D-type identifier which identifies the DVI connector as a DVI-D-type connector when the output of the 22nd of the DVI connector of the Terminal Display Management System clock ground is at a ground level and the output of the C5-th pin of the DVI connector of the analog ground is at a high impedance level; and a DVI-I-type identifier which identifies the DVI connector as a DVI-I-type connector when the output of the 22nd of the DVI connector of the Terminal Display Management System clock ground and the output of the C5-th pin of the DVI connector of the analog ground are at a ground level [see Fig. 12, column 1, line 54 to column 2, line 8, conventionally disclosing, a table of a DVI-D connector pin conventionally assigns at least a pin C5 is analog ground, and a pin 22 is TMDS clock shield].

8. As to claim 6, Shirasaki conventionally discloses the apparatus of claim 4, wherein the display identification data output command unit comprises: an analog display identification data output commander which commands the analog display

identification data storing and outputting unit to output the analog display identification data when the DVI connector is identified as a DVI-A-type connector by the connector type identification unit; and a digital display identification data output commander which commands the digital display identification data storing and outputting unit to output the digital display identification data when the DVI connector is identified as a DVI-D-type connector by the connector type identification unit [the user makes setting/command on the switcher for the analog data and the digital data, see Fig. 13 and claim 1 above].

9. As to claim 7, Shirasaki further discloses the apparatus of claim 6, wherein the analog display identification data output commander commands the analog display identification data storing and outputting unit to output the analog display identification data according to predetermined additional information when the DVI connector is identified as a DVI-I-type connector by the connector type identification unit [see Fig. 1, col. 5, line 57 to column 6, line 49, disclosing, the predetermined additional information comprises at least analog red, green, and blue data, and horizontal and vertical sync. See Fig. 13, col. 38-50, conventionally disclosing, the user makes a selection for the analog display identification data on the switcher].

10. As to claim 8, Shirasaki further discloses the apparatus of claim 7, wherein the additional information indicates that the analog display identification data is to be output according to a user selection [the user makes a selection for the analog display identification data on the switcher, see Fig. 13, col. 38-50, and claim 7 above].

11. As to claim 9, Shirasaki further discloses the apparatus of claim 6, wherein the digital display identification data output commander commands the digital display

identification data storing and outputting unit to output the digital display identification data according to predetermined additional information when the DVI connector is identified as a DVI-I-type connector by the connector type identification unit [see Fig. 10, column 16, disclosing, the predetermined additional information comprises at least digital red, green, and blue data, and horizontal and vertical sync. See Fig. 13, col. 38-50, conventionally disclosing, the user makes a selection for the digital display identification data on the switcher].

12. As to claim 10, Shirasaki further discloses the apparatus of claim 9, wherein the predetermined additional information indicates that the digital display identification data is to be output according to a user selection [the user makes a selection for the digital display identification data on the switcher, see Fig. 13, col. 38-50, and claim 9 above].

13. The limitation of claim 11, are similar to those of claim 1, though in method form, therefore the rejection of claim 11, will be treated using the same rationale as claim 1.

14. Claim 12 shares the same limitations as those of claim 2 and therefore the rationale for rejection will be the same.

15. Claim 13 shares the same limitations as those of claim 3 and therefore the rationale for rejection will be the same.

16. Claim 14 shares the same limitations as those of claim 4 and therefore the rationale for rejection will be the same.

17. Claim 15 shares the same limitations as those of claim 5 and therefore the rationale for rejection will be the same.

18. Claim 16 shares the same limitations as those of claim 6 and therefore the rationale for rejection will be the same.

19. Claim 17 shares the same limitations as those of claim 7 and therefore the rationale for rejection will be the same.

20. Claim 18 shares the same limitations as those of claim 8 and therefore the rationale for rejection will be the same.

21. Claim 19 shares the same limitations as those of claim 9 and therefore the rationale for rejection will be the same.

22. Claim 20 shares the same limitations as those of claim 10 and therefore the rationale for rejection will be the same.

23. The limitation of claim 21, are similar to those of claim 1, though in computer-readable recording medium form, therefore the rejection of claim 21, will be treated using the same rationale as claim 1.

24. Claim 22 shares the same limitations as those of claim 2 and therefore the rationale for rejection will be the same.

25. Claim 23 shares the same limitations as those of claim 3 and therefore the rationale for rejection will be the same.

26. Claim 24 shares the same limitations as those of claim 4 and therefore the rationale for rejection will be the same.

27. Claim 25 shares the same limitations as those of claim 5 and therefore the rationale for rejection will be the same.

28. Claim 26 shares the same limitations as those of claim 6 and therefore the rationale for rejection will be the same.

29. Claim 27 shares the same limitations as those of claim 7 and therefore the rationale for rejection will be the same.

30. Claim 28 shares the same limitations as those of claim 8 and therefore the rationale for rejection will be the same.

31. Claim 29 shares the same limitations as those of claim 9 and therefore the rationale for rejection will be the same.

32. Claim 30 shares the same limitations as those of claim 10 and therefore the rationale for rejection will be the same.

33. The limitations of claim 31 are the same as those of claim 1 and therefore the claim will be rejected using the same rationale.

34. Claim 32 shares the same limitations as those of claim 6 and therefore the rationale for rejection will be the same.

35. Claim 33 shares the same limitations as those of claim 2 and therefore the rationale for rejection will be the same.

36. Claim 34 shares the same limitations as those of claim 3 and therefore the rationale for rejection will be the same.

37. Claim 35 shares the same limitations as those of claim 4 and therefore the rationale for rejection will be the same.

38. Claim 36 shares the same limitations as those of claim 5 and therefore the rationale for rejection will be the same.

39. Claim 37 shares the same limitations as those of claim 6 and therefore the rationale for rejection will be the same.

40. Claim 38 shares the same limitations as those of claim 7 and therefore the rationale for rejection will be the same.

41. Claim 39 shares the same limitations as those of claim 8 and therefore the rationale for rejection will be the same.

42. Claim 40 shares the same limitations as those of claim 9 and therefore the rationale for rejection will be the same.

43. Claim 41 shares the same limitations as those of claim 10 and therefore the rationale for rejection will be the same.

Conclusion

44. Any inquiry concerning this communication or earlier communications from the examiner should be directed to KEVIN M. NGUYEN whose telephone number is 571-272-7697. The examiner can normally be reached on MON-THU from 8:00-6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, a supervisor RICHARD A. HJERPE can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8000.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the Patent Application Information Retrieval system, see http://portal.uspto.gov/external/portal/pair. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin M. Nguyen

Kevin M. Nguyen
Patent Examiner
Art Unit 2629

KMN
May 14, 2006